

FIG. 1

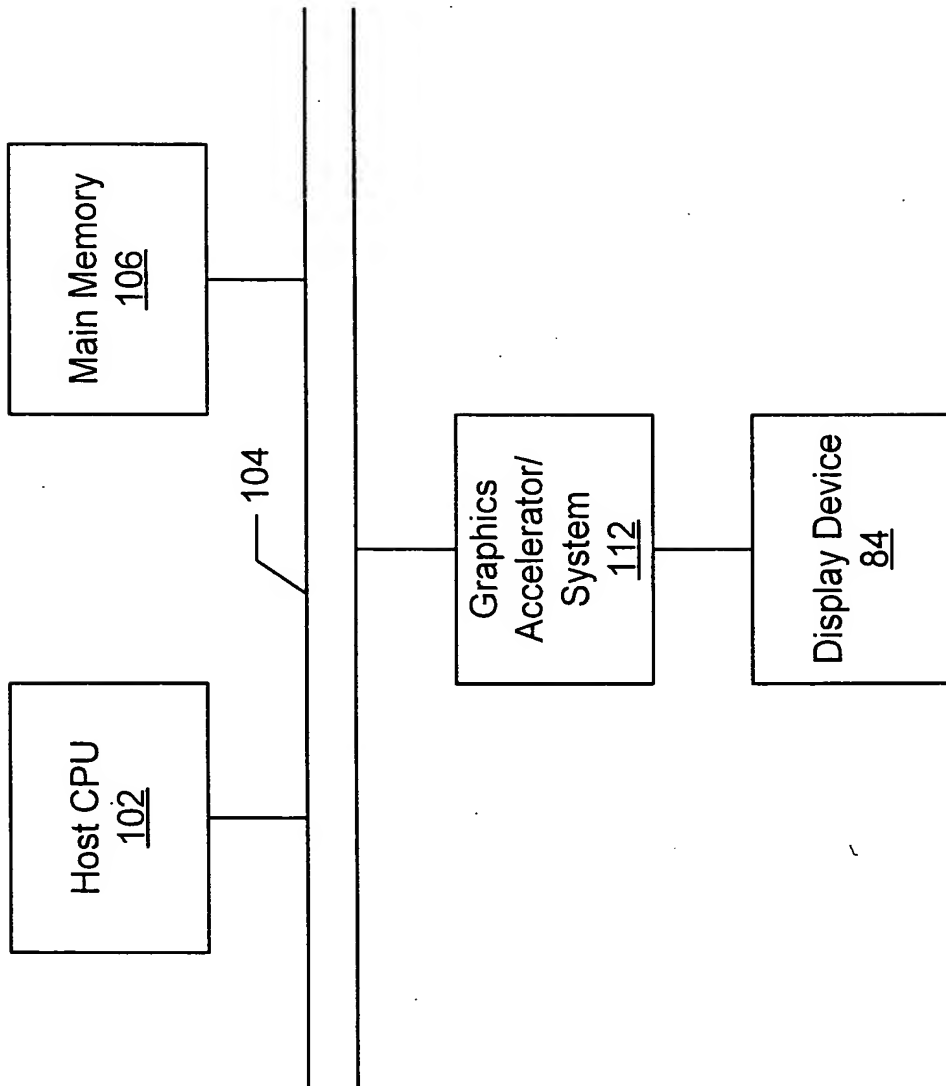


FIG. 2

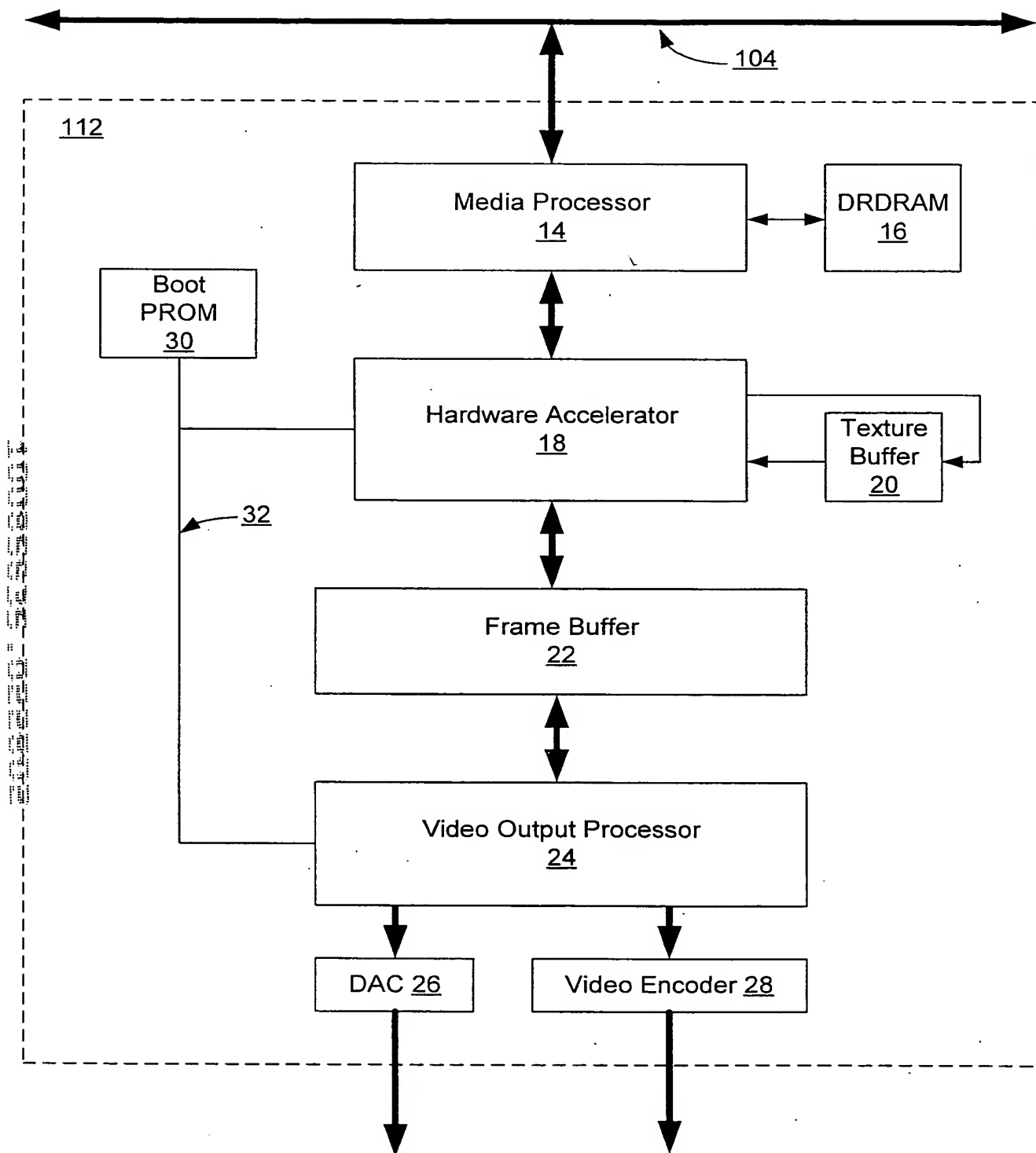


FIG. 3

FIG. 4 is a block diagram of a system 14, according to one embodiment of the present invention. The system 14 includes a Host Interface 11, a Direct Port, an Accelerated Port, a Graphics Queue 148, a Geometry Data Preprocessor 150, a BIU 154, a RAC 156, DRDRAM 16, MPU1 152B, MPU2 152A, and a Controller 160.

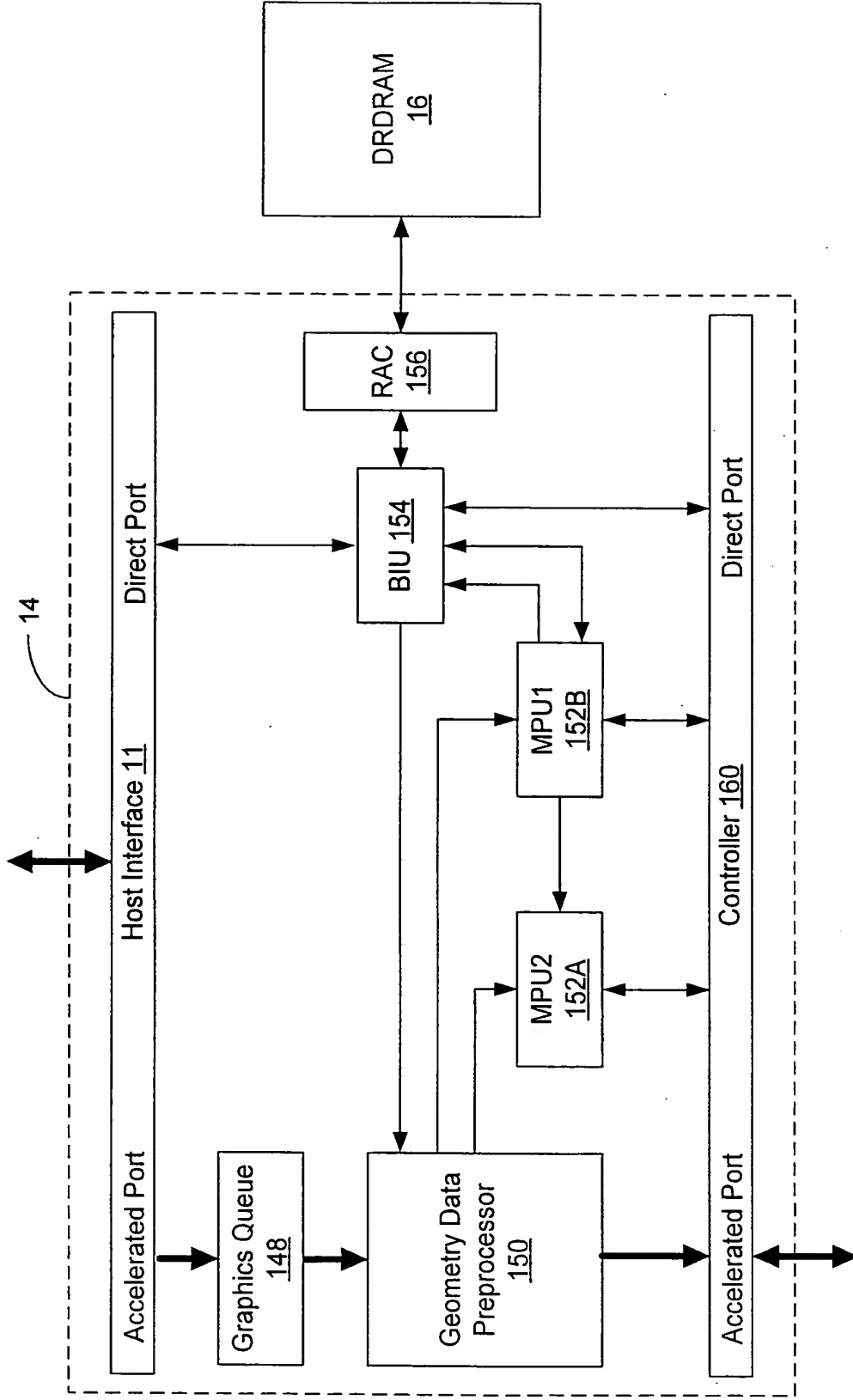


FIG. 4

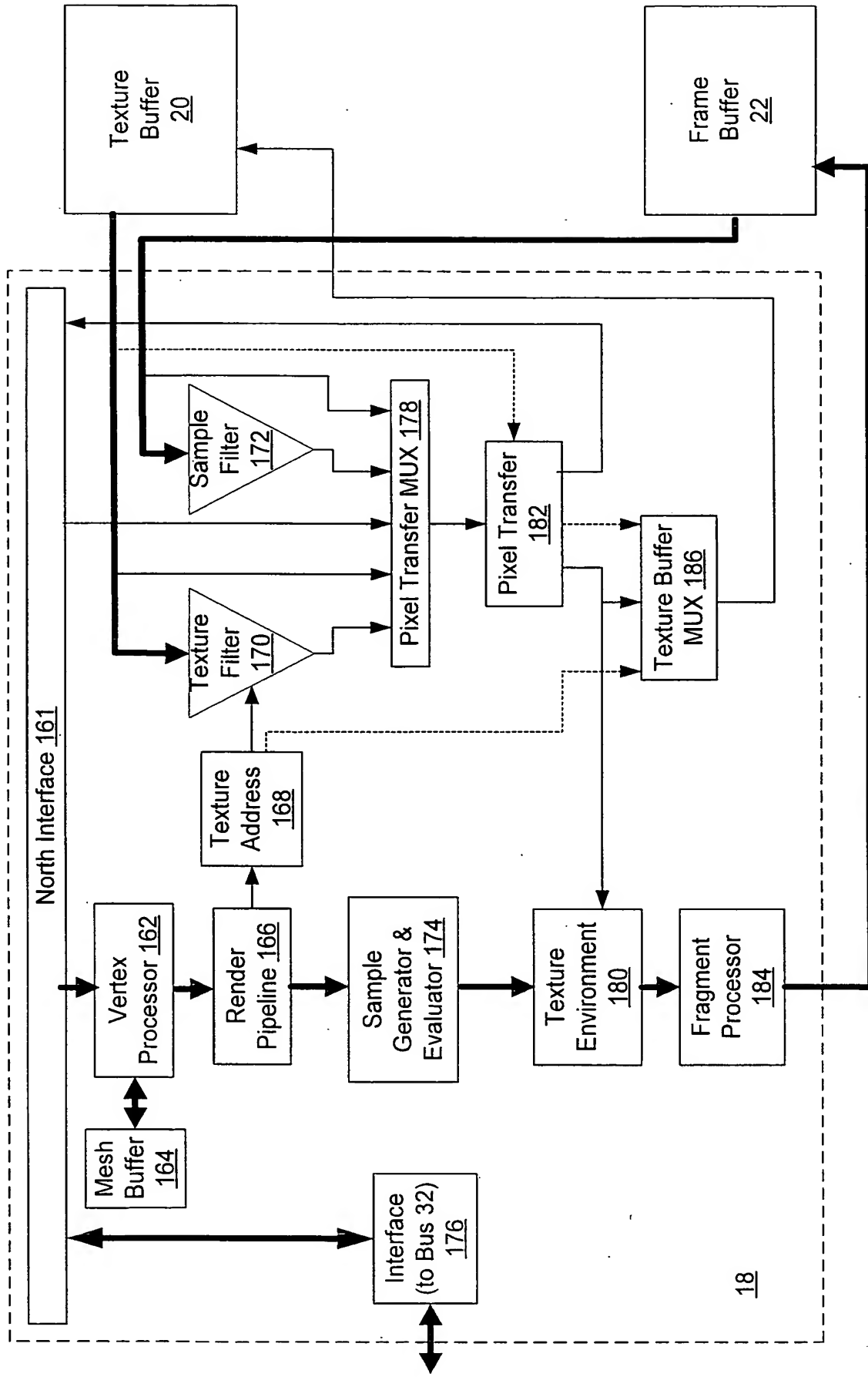


FIG. 5

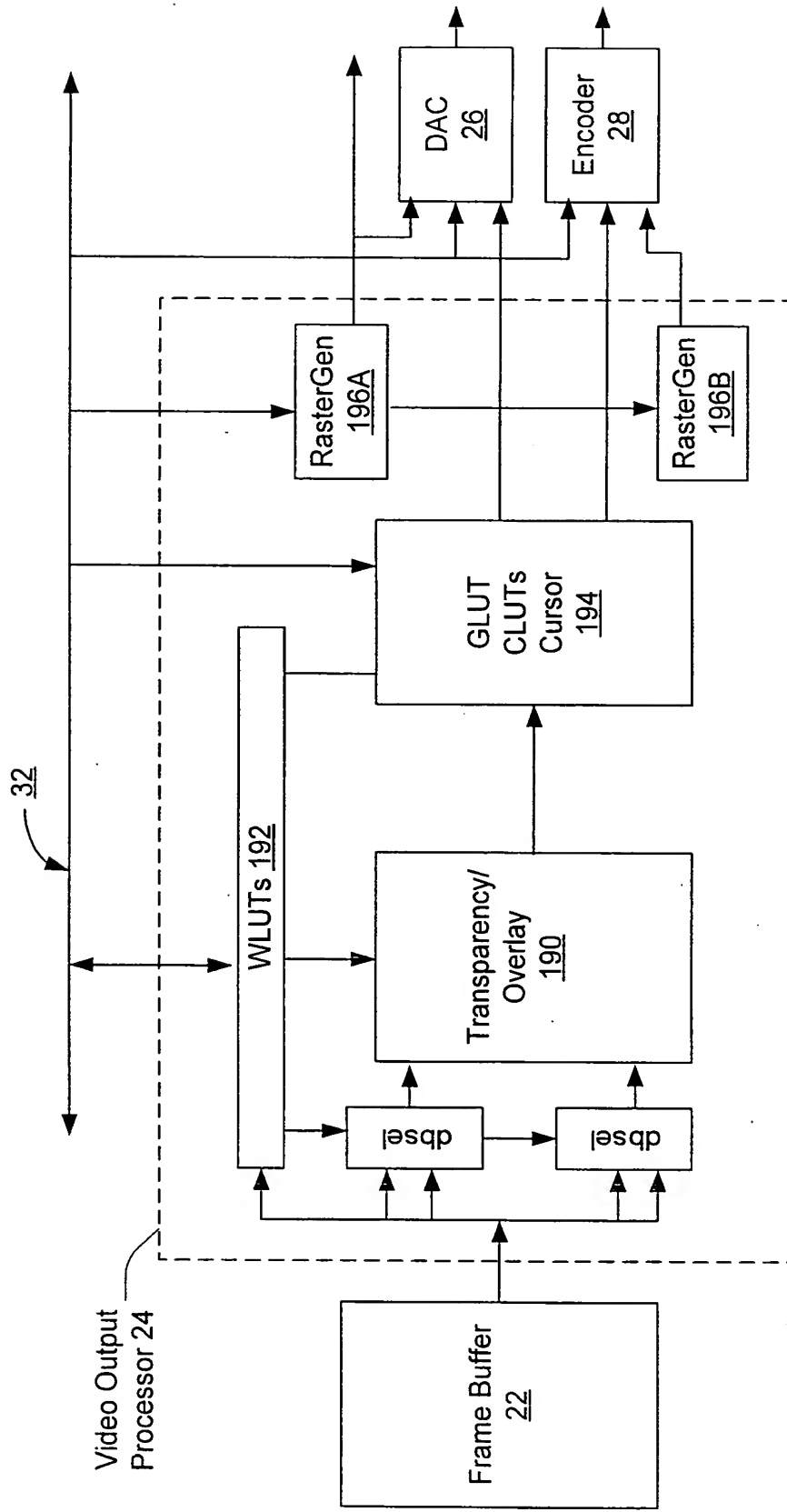
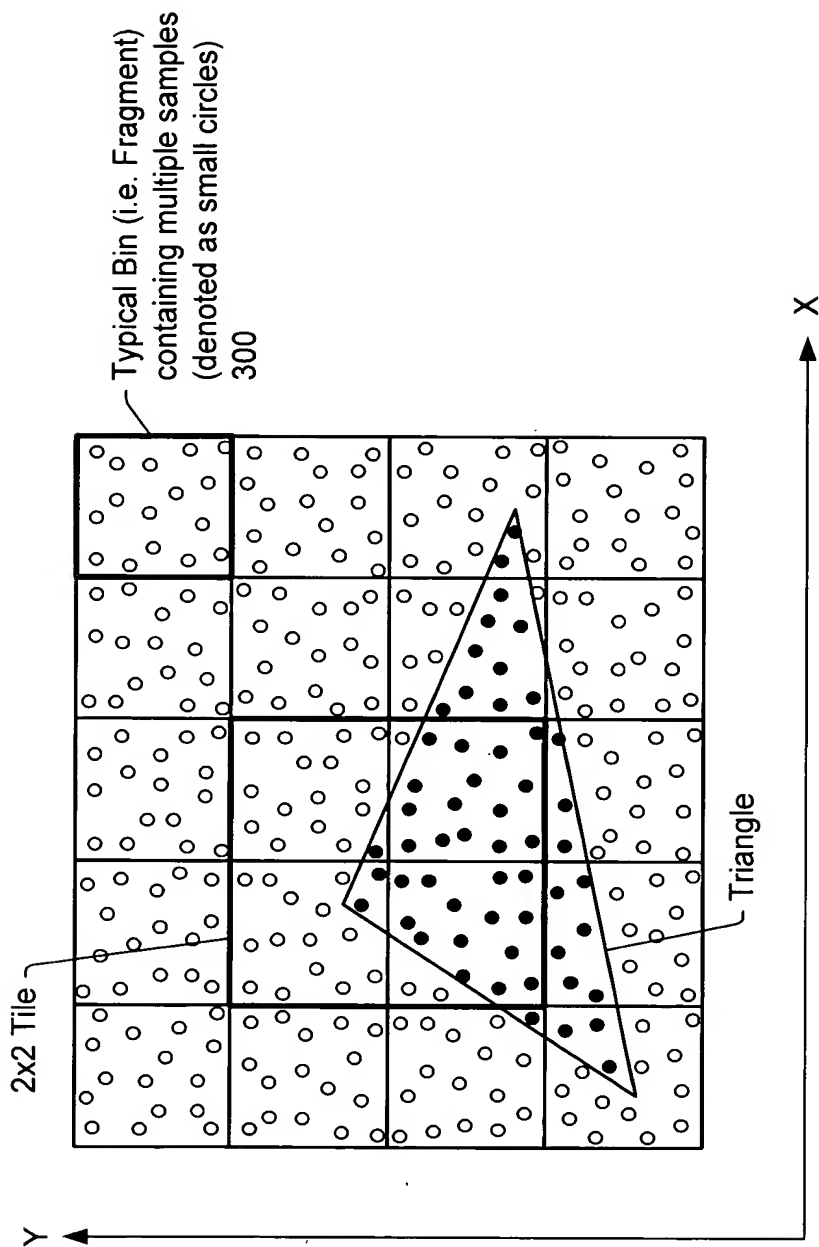


FIG. 6

FIG. 7 is a diagram illustrating a portion of a sample space. The diagram shows a grid of 2x2 tiles, each containing a distribution of small circles representing samples. A triangle is overlaid on the grid, indicating a specific region of interest. The x and y axes are shown, with the x-axis pointing to the right and the y-axis pointing upwards.

Portion of Sample Space



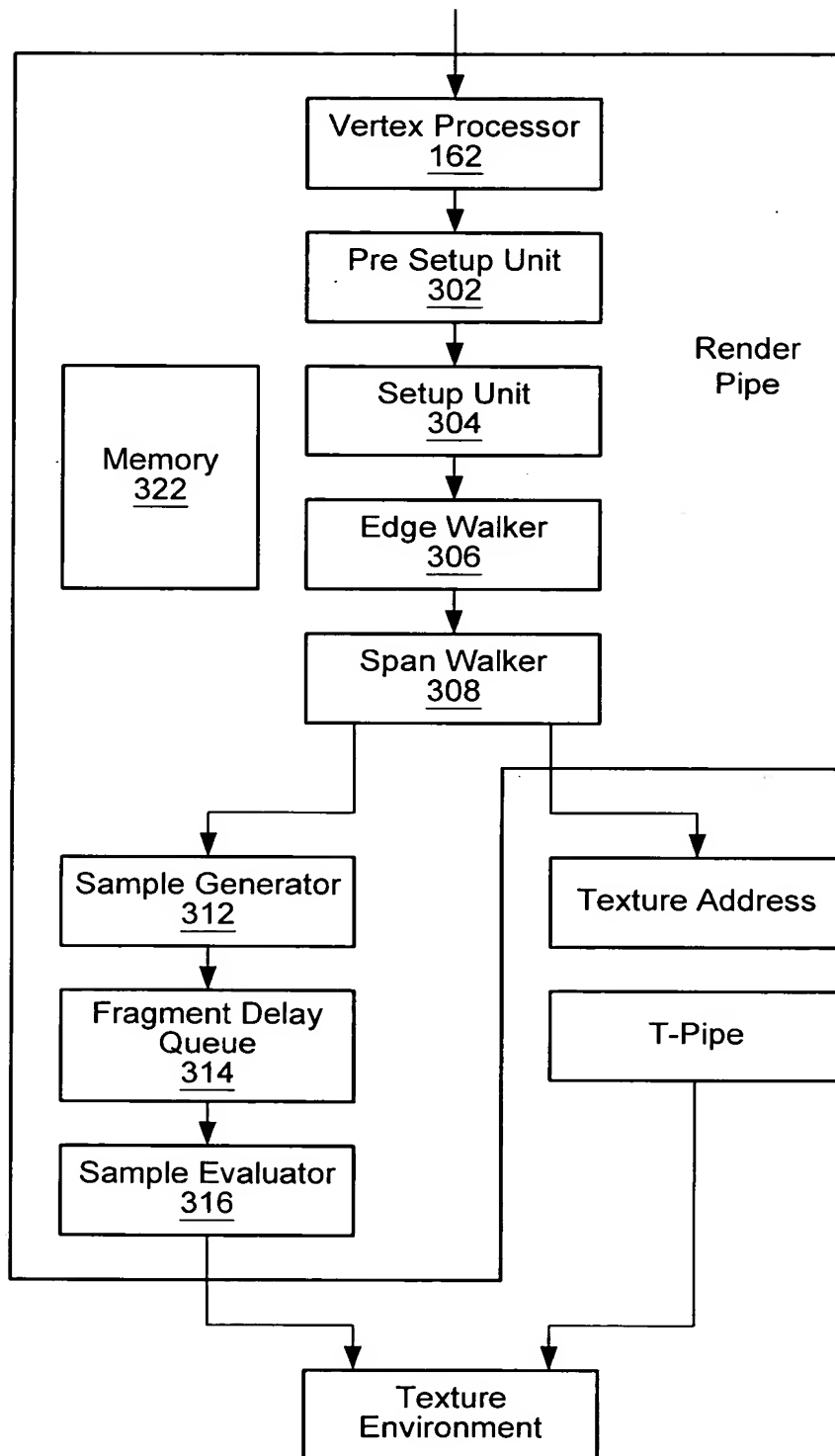
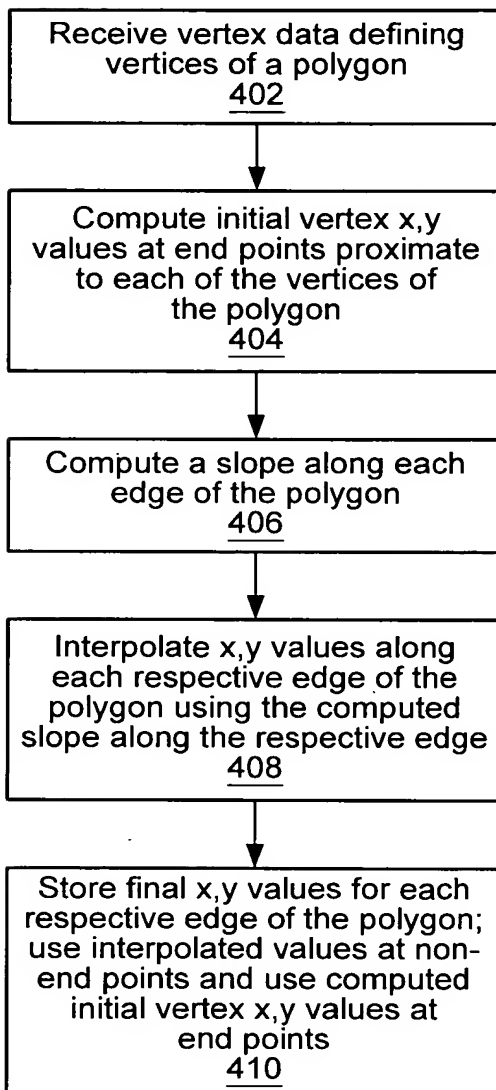


Fig. 8





*Fig. 9*

$P_{se}, P_{sm}, P_{me}$  are the initial edge intercepts

Diagram illustrating a triangle  $MPE$  on a grid. The vertices are  $M$  (top),  $P$  (bottom left), and  $E$  (bottom right). The edges are labeled:  $x$ -major (edge  $MP$ ),  $y$ -major (edge  $ME$ ), and controlling edge (edge  $PE$ ). The initial edge intercepts are marked on the axes:  $P_{se}$  on the x-axis,  $P_{sm}$  on the y-axis, and  $P_{me}$  on the x-axis. The origin is labeled  $S$ .

